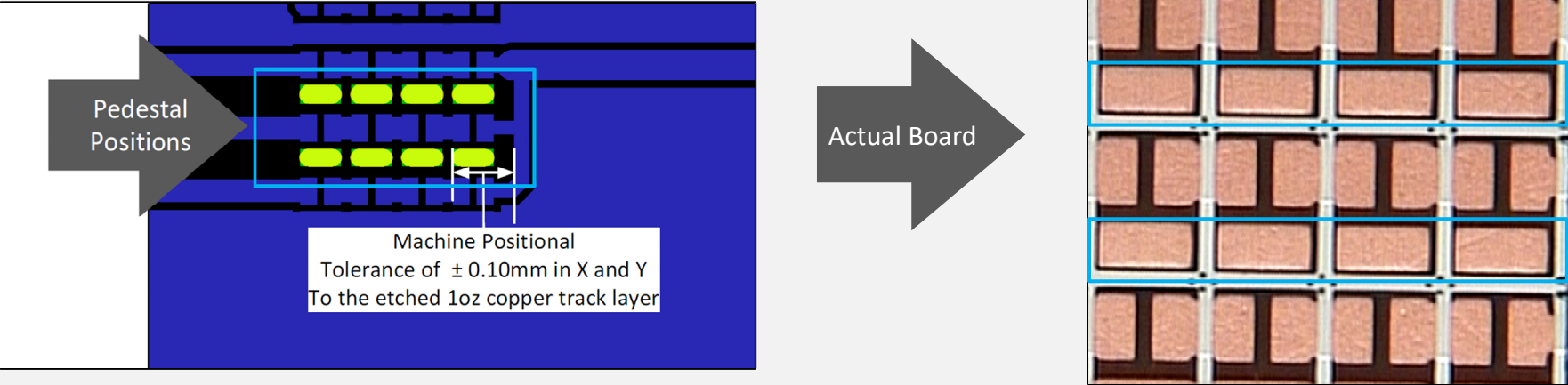


Pedestal PCB – Technical Specification Guidelines

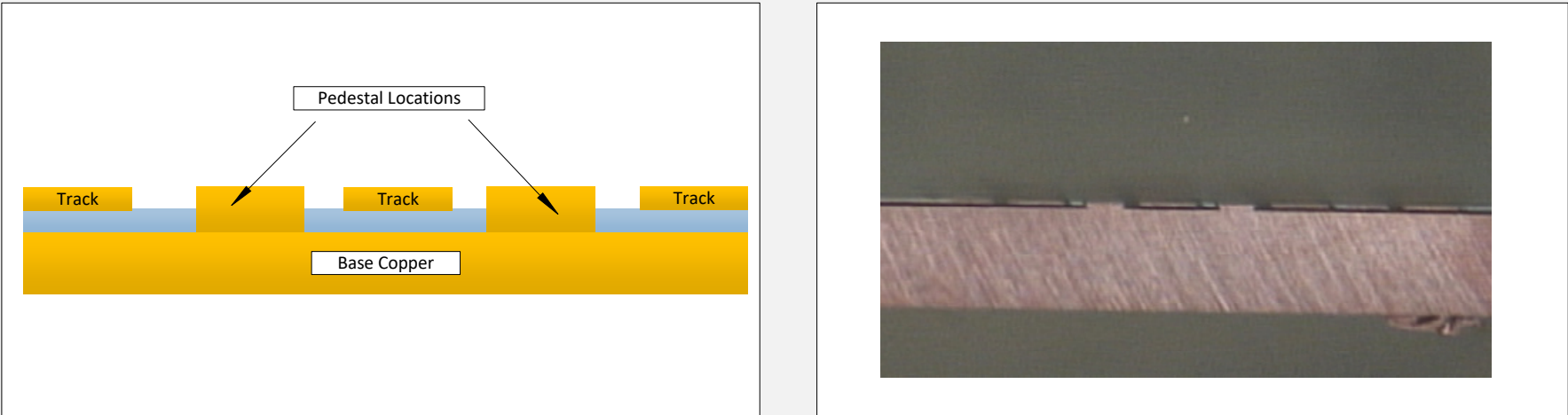
Track Widths / Gaps		Standard	Special
Minimum Track Width (base copper) (If outside our standard capability, a contract review meeting will take place)	17 µm (0.5 oz)	0.10	<0.10
	35 µm (1 oz)	0.10	<0.10
	70 µm (2 oz)	0.20	<0.20
Minimum Gap Between Tracks (In dense areas of tracking, preference should be given to gap size over track size)	17 µm (0.5 oz)	0.10	< 0.10
	35 µm (1 oz)	0.15	< 0.15
	70 µm (2 oz)	0.20	< 0.20
	105 µm (3 oz)	0.25	< 0.25
	140 µm (4 oz)	0.40	< 0.40

Hole Data		Standard	Special
Minimum Hole Size		1.20	N/A
Hole Tolerance - Drilled	< 6.50 mm	± 0.05	± 0.03
Hole Tolerance - Routed Holes	> 6.50 mm	± 0.10	+0 /-0.05
Minimum Hole to Edge		0.10	< 0.10
Hole to Edge Tolerance		± 0.15	± 0.10
Minimum Radial Clearance Pad (Annulus)		0.1	0.75

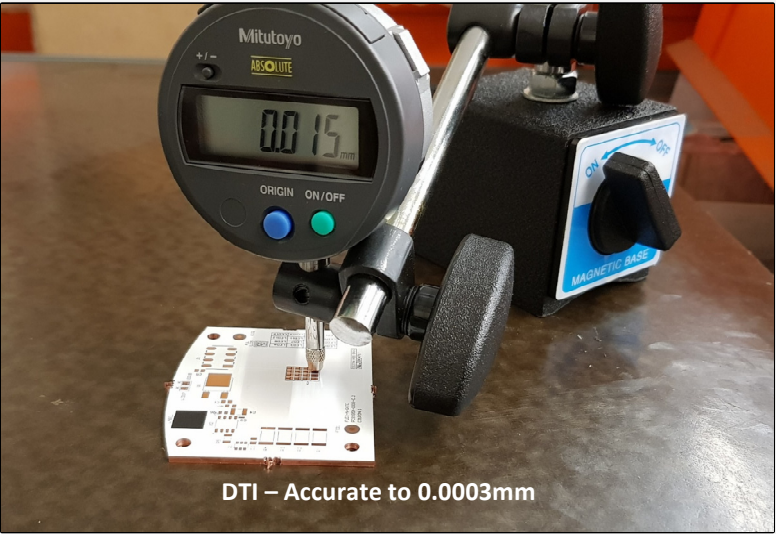
Positional Accuracy



Microsection of the pedestal position



Surface Flatness



DTI – Accurate to 0.0003mm

Align DTI gauge on the tracking layer and zero the unit.

Take a reading of each pedestal location

Log measurement

Measurement Result's (mm)



Measurement Result's (mm)
+0.016
+0.008
+0.009
+0.009
+0.010
+0.014
+0.016
+0.015

Pedestal Size

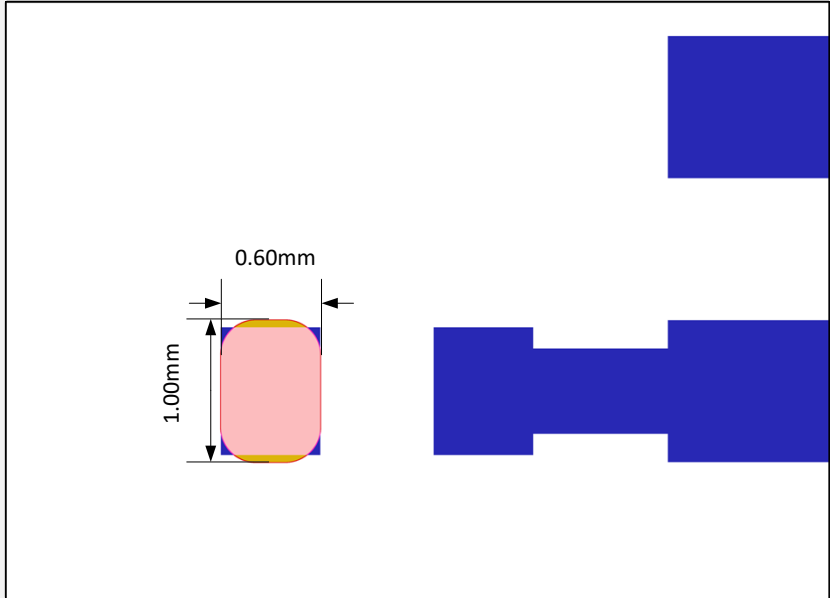


Fig. 1 Minimum Pedestal Size.

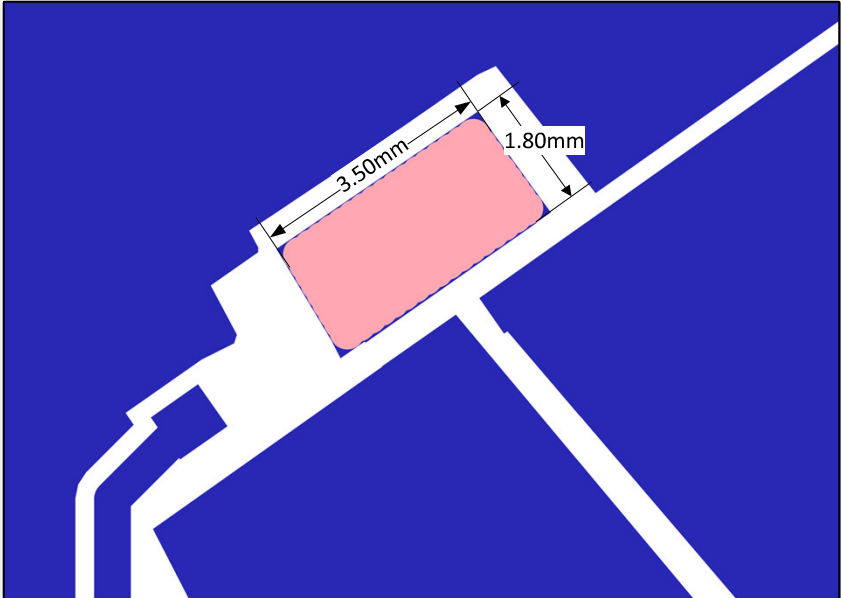


Fig. 2 Maximum Pedestal Size.

Dielectric Thickness

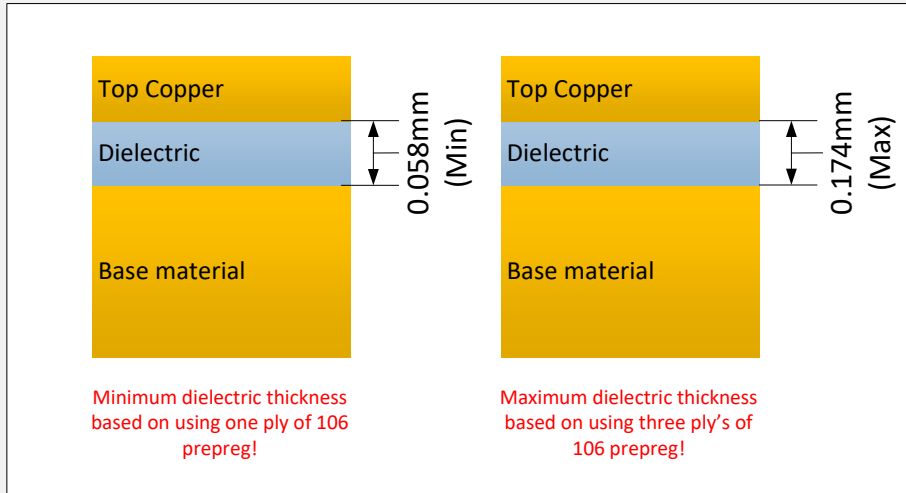


Fig. 3 Minimum and maximum dielectric thickness.

Gap between Pedestal and Track

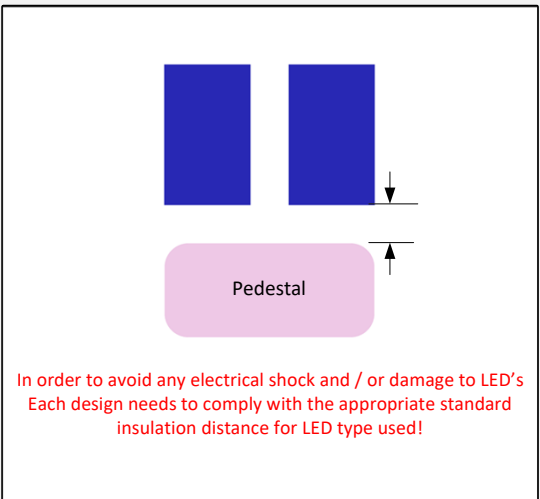


Fig. 4 Minimum gap between pedestal and anode pad.

Number of traces between LED's

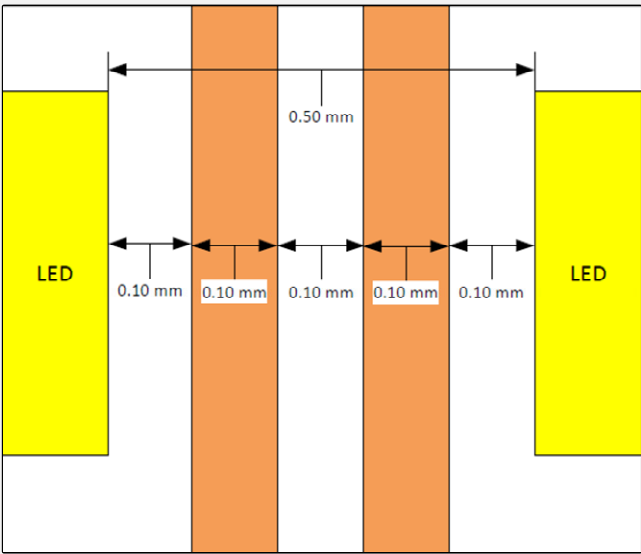
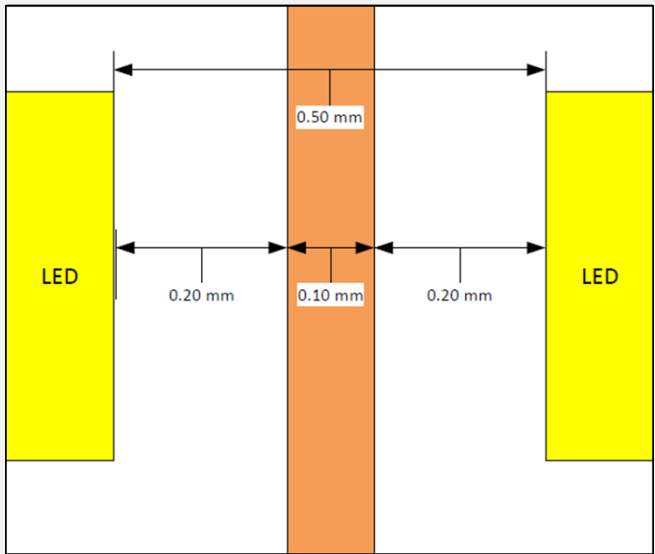


Fig. 5 Minimum track and gap between LED positions.